WEST Search History

| Hide Items | Restore | Clear | Cancel |

DATE: Thursday, February 01, 2007

Hide?	Set Name	Query	Hit Count
		DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR	
☐	L28	L27 and "code module"	11
☐	L27	L26 and "chipset"	78
☐	L26	L25 and (asymmetric key)	217
☐	L25	L24 and L23	217
☐	L24	(decrypt$3 or decod$3 or decipher$3) same (signature)	10551
☐	L23	(key) same (embed$4) same (chipset or integrated circuit)	2208
☐	L22	713/176.ccls.	2223
☐	L21	713/181.ccls.	365
☐	L20	726/32.ccls.	87
☐	L19	L18 and L17	1
☐	L18	(embed$3) near (chipset or integrated circuit)	6816
☐	L17	(decrypt$3 or decod$3 or decipher$3) near (signature) near (key)	116
☐	L16	(decrypt$3 or decod$3 or decipher$3) near (signature) near (key) near (embed$3)	0
		DB=PGPB; PLUR=YES; OP=OR	
☐	L15	L10 and "asymmetric"	0
☐	L14	L10 and "public"	0
☐	L13	L10 and "public key"	0
☐	L12	L11 and "public"	0
☐	L11	L10 and "BIOS"	0
☐	L10	L8 and "FPGA"	1
☐	L9	L8 and "chipset"	0
☐	L8	L7 and (embed$3)	1
☐	L7	L2 and "key"	1
☐	L6	L2 and "code"	0
☐	L5	L4 and "key"	0
☐	L4	L3 and "code"	0
☐	L3	L2 and "signature"	0
☐	L2	L1 and (decrypt$3)	1
☐	L1	20020150252	1

END OF SEARCH HISTORY

Google

Web   Images   Video   News   Maps   more »

signature, embed, asymmetric, chipset, auther   [Search]   Advanced Search
Preferences

Web Results **1** - **10** of about **98** for <u>signature</u>, <u>embed</u>, <u>asymmetric</u>, <u>chipset</u>, <u>authentication</u> <u>"code</u> <u>module"</u>.

Authenticated **code module** - Patent 20030126442
The **code module** of claim 2, wherein the **signature** comprises a digest value ... an
asymmetric key **embedded** in a hardware component of the computing device. ...
www.freepatentsonline.com/20030126442.html - 71k - Cached - Similar pages

aaa abdominal aortic aneurysm aaa access all areas
XPE eXtreme Performance Enhancement XPE [Microsoft Windows] XP **Embedded** XPF
eXPlosion-release Factor XPF eXtended Packet Filter XPG eXcellent Personal ...
www.xs4all.nl/~jtv/gtf/GPL_TLA_FAQ - 605k - Cached - Similar pages

[PDF] The Intel Safer Computing Initiative
File Format: PDF/Adobe Acrobat
key for the symmetric algorithm uses **asymmetric**, where the speed is not ... **chipset**-
specific authenticated **code module**. This module is tests for an ...
www.intel.com/intelpress/validation100/secc/SECC_100Validation.pdf - Similar pages

rootr.net : man : c (1)
Lets you **embed** Perl code into HTML documents. Perl code is evaluated at the server ...
ggFreeModule, ggGetSymbolAddress (3) - Portable **code module** loading ...
www.root-route.org/man/man/c++/1 - 886k - Cached - Similar pages

rootr.net : man : G(2)
Pod::Usage, pod2usage() (3p) - print a usage message from **embedded** pod ...
RSA_padding_check_none (3) - **asymmetric** encryption padding RSA_print, ...
www.root-route.org/man/man/G/2 - 250k - Supplemental Result - Cached - Similar pages

rootr.net : man : s(3p)
S (3o) - The output **signature** of the functor Hashtbl.Make. ... Lets you **embed** Perl code
into HTML documents. Perl code is evaluated at the server side and ...
www.rootr.net/man/man/s/3p - 811k - Cached - Similar pages

rootr.net : man : O(1)
... RSA_padding_check_none (3) - **asymmetric** encryption padding RSA_print, ... DECchip
21072/21071 Core Logic **chipset** apio (4/SPARC64) - SBus Aurora pio1 ...
www.rootr.net/man/man/O/1 - 250k - Supplemental Result - Cached - Similar pages

From horms at verge.net.au Sun Oct 1 04:42:01 2006 From: horms at ...
Also, it relies on clocks being synchronized, since **authentication** will fail if the UTC ... SPA
packets are non-replayable in both **asymmetric** and symmetric ...
lists.netfilter.org/pipermail/netfilter-devel/2006-October.txt - 694k - Cached - Similar pages

rootr.net : man : N(1)
... DSA_SIG_free (3) - allocate and free DSA **signature** objects DSA_do_sign, ...
RSA_padding_check_none (3) - **asymmetric** encryption padding RSA_print, ...
sshfs.net/man/man/N/1 - 250k - Supplemental Result - Cached - Similar pages

rootr.net : man : r(0)
... DECchip 21072/21071 Core Logic **chipset** apio (4/SPARC64) - SBus Aurora pio1 ...
auth_verify (3) - simplified interface to the BSD **Authentication** system ...
www.rroute.com/man/man/r/0 - 250k - Supplemental Result - Cached - Similar pages

---

Result Page: **1** 2 3 4   **Next**

Try Google Desktop: search your computer as easily as you search the web.

---

signature, embed, asymmetric, chips [Search]

Search within results | Language Tools | Search Tips | Dissatisfied? Help us improve

---

Google Home - Advertising Programs - Business Solutions - About Google

©2007 Google